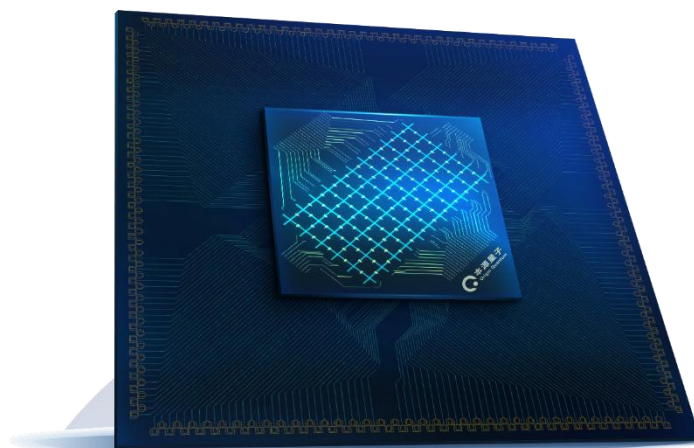


‘Wukong’ series Superconducting Quantum Chips



‘Wukong’ series superconducting quantum chips built based on circuit quantum electrodynamics system is the core device of superconducting quantum computer processing quantum algorithms . High fidelity quantum logic gate operations can be realized, and then enable the execution of quantum programs

Features

- ✧ Support the customization of superconducting quantum chip products with more than 60-qubit
- ✧ Support X, Y, Z, S, H and other single-qubit gate operations
- ✧ Support CZ, CNOT and other double-qubit gate operations
- ✧ Support independent or joint reading of qubits
- ✧ The chip adopts mature nanomachining technology, which can achieve mass production

Specifications

Performance Parameters	
Chip type	General Quantum chip Based on Standard Gate Operation
Readout	Independent or Joint Reading
Relaxation time T1	$\geq 15\mu\text{s}$ (At the maximum frequency of qubits)
Decoherence time T2*	$\geq 7\mu\text{s}$ (At the maximum frequency of qubits)
Single-qubit gate operation time	30ns
Double-qubit gate operation time	60ns
Single-qubit gate average fidelity	$\geq 99.9\%$
Double-qubit gate average fidelities	$\geq 98\%$
Readout average fidelity	$\geq 95\%$